



Intel® Desktop Board D845BG Specification Update

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Order Number: A83340-002

The Intel® Desktop Board D845BG may contain design defects or errors known as errata which may cause the product to deviate from published specifications. Current characterized errata are documented in this Specification Update.

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The Intel® desktop board D845BG may contain design defects or errors known as errata which may cause the product to deviate from published specifications. Current characterized errata are available on request.

Contact your local Intel sales office or your distributor to obtain the latest specifications before placing your product order.

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REVISION HISTORY

Date of Revision	Version	Description
January 2002	-001	This document is the first Specification Update for the Intel® Desktop Board D845BG.
February 2002	-002	Added Erratum 1. Updated General Information.

PREFACE

This document is an update to the specifications contained in the *Intel® Desktop Board D845BG Technical Product Specification* (Order number *A75852*). It is intended for hardware system manufacturers and software developers of applications, operating systems, or tools. It will contain Specification Changes, Errata, Specification Clarifications, and Documentation Changes.

Refer to the *Intel® Pentium® 4 Processor Specification Update* (Order number 249199) for specification updates concerning the Intel Pentium 4 processor. Items contained in the *Intel Pentium 4 Processor Specification Update* that either do not apply to the desktop board D845BG or have been worked around are noted in this document. Otherwise, it should be assumed that any processor errata for a given stepping are applicable to the PBA revision(s) associated with that stepping.

Refer to the *Intel® 82845 Chipset: 82845 Memory Controller Hub (MCH) Specification Update* (Order Number 290659) for specification updates concerning the 82845 MCH Controller. Items contained in the *82845 MCH Specification Update* that either do not apply to the desktop board D845BG or have been worked around are noted in this document. Otherwise, it should be assumed that any MCH errata for a given stepping are applicable to the PBA revision(s) associated with that stepping.

Refer to the *Intel® 82801 I/O Controller Hub (ICH) Specification Update* (Order Number 290677) for specification updates concerning the 82801 I/O Controller Hub. Items contained in the *Intel 82801 ICH Specification Update* that either do not apply to the desktop board D845BG or have been worked around are noted in this document. Otherwise, it should be assumed that any ICH errata for a given stepping are applicable to the Printed Board Assembly (PBA) revision(s) associated with that stepping.

Refer to the *Intel® 82802 Firmware Hub (FWH) Specification Update* (Order Number TBD) for specification updates concerning the 82802 Firmware Hub. Items contained in the *Intel 82802 FWH Specification Update* that either do not apply to the desktop board D845BG or have been worked around are noted in this document. Otherwise, it should be assumed that any ICH errata for a given stepping are applicable to the Printed Board Assembly (PBA) revision(s) associated with that stepping.

Nomenclature

Specification Changes are modifications to the current published specifications. These changes will be incorporated in the next release of the specifications.

Errata are design defects or errors. Characterized errata may cause the desktop board D845BG's behavior to deviate from published specifications. Hardware and software designed to be used with any given Printed Board Assembly (PBA) and BIOS revision level must assume that all errata documented for that PBA and BIOS revision level are present on all desktop boards.

Specification Clarifications describe a specification in greater detail or further highlight a specification's impact to a complex design situation. These clarifications will be incorporated in the next release of the specifications.

Documentation Changes include typos, errors, or omissions from the current published specifications. These changes will be incorporated in the next release of the specifications.

Specification Update for the Intel® Desktop Board D845BG

GENERAL INFORMATION

Basic Desktop Board D845BG Identification Information

AA Revision	PBA Revision	BIOS Revision	Notes
A65261-402	A65262-402	PT84510A.86A.0012.P01	1-6
A65251-402	A65252-402	PT84510A.86A.0012.P01	1-6
A65251-403	A65252-403	PT84510A.86A.0012.P01	1-6
A65249-402	A65250-402	PT84510A.86A.0012.P01	1-6
A65249-403	A65250-403	PT84510A.86A.0012.P01	1-6
A74060-402	A74059-402	PT84510A.86A.0012.P01	1-6
A74060-403	A74059-403	PT84510A.86A.0012.P01	1-6
A74063-402	A74062-402	PT84510A.86A.0012.P01	1-6
A74063-403	A74062-403	PT84510A.86A.0012.P01	1-6

NOTES:

- The PBA number or AA number is found on a small label on the component side of the board.
- The 82845 Chipset kit used on this PBA revision consists of three components as follows:

Device	Stepping	S-Spec Numbers
82845 MCH	B0	SL5YQ
82801BA ICH	B5	SL5WK
82802AB 4Mbit FWH	A1	SB48

- The following errata are contained in the *Intel® Pentium® 4 Processor Specification Update* (Order Number 249199) for the Pentium 4 processor and either do not apply to the desktop board D845BG or have been worked-around in this PBA and/or BIOS revision: None. All other errata associated with the processor apply to this PBA revision.
- The following items are contained in the *Intel® 82845 Memory Controller Hub (MCH) Specification Update* (Order Number 290659) and either do not apply to the desktop board D845BG or have been worked around in this PBA and/or BIOS revision: None. All other errata associated with the MCH apply to this PBA revision.
- The following items are contained in the *Intel® 82801 I/O Controller Hub Specification Update* (Order Number 290677) and either do not apply to the desktop board D845BG or have been worked around in this PBA and/or BIOS revision: None. All other errata associated with the ICH apply to this PBA revision.
- The following items are contained in the *Intel® 82802 Firmware Hub Specification Update* (Order Number TBD) and either do not apply to the desktop board D845BG or have been worked around in this PBA and/or BIOS revision: None. All other errata associated with the FWH apply to this PBA revision.

Summary Table of Changes

The following table indicates the Specification Changes, Errata, Specification Clarifications, or Documentation Changes that apply to the desktop board D845BG. Intel intends to fix some of the errata in a future revision of the desktop board, and to account for the other outstanding issues through documentation or specification changes as noted. This table uses the following notations:

CODES USED IN SUMMARY TABLE

Doc:	Document change or update that will be implemented.
Fix:	This erratum is intended to be fixed in a future revision of the desktop board, driver, or BIOS.
Fixed:	This erratum has been previously fixed.
NoFix:	There are no plans to fix this erratum.
Shaded:	This erratum is either new or modified from the previous version of the document.

NO.	PLANS	ERRATA
1	Fix	System hang during POST may occur when using certain USB cameras

ERRATA

1. ***System Hang During POST May Occur When Using Certain USB Cameras***

PROBLEM: During the system boot, certain USB cameras may cause a hang during POST if the camera is on during the boot process.

IMPLICATION: Some USB cameras may cause a system hang if the camera is on during system boot due to the BIOS incorrectly identifying the camera as a bootable device.

WORKAROUND: Ensure that the USB camera is off during the system boot process.

STATUS: This erratum may be fixed in a future BIOS revision.